UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/615,738	07/09/2003	Roger S. Twede	100203290-1 1826		
	7590 11/03/200 CKARD COMPANY	EXAMINER			
P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			MCLEAN, NEIL R		
			ART UNIT	PAPER NUMBER	
			2625		
			NOTIFICATION DATE	DELIVERY MODE	
			11/03/2008	ELECTRONIC	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

JERRY.SHORMA@HP.COM mkraft@hp.com ipa.mail@hp.com

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/615,738	TWEDE, ROGER S.		
Examiner	Art Unit		

	Nell K. McLean	2023	
The MAILING DATE of this communication appe	ars on the cover sheet with the	correspondence add	ress
THE REPLY FILED 14 October 2008 FAILS TO PLACE THIS A	PPLICATION IN CONDITION FO	R ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appelor Continued Examination (RCE) in compliance with 37 C periods:	replies: (1) an amendment, affidav eal (with appeal fee) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing	date of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this Arno event, however, will the statutory period for reply expire la Examiner Note: If box 1 is checked, check either box (a) or (IMONTHS OF THE FINAL REJECTION. See MPEP 706.07(f	ater than SIX MONTHS from the mailin b). ONLY CHECK BOX (b) WHEN THE).	g date of the final rejection FIRST REPLY WAS FI	on. LED WITHIN TWO
Extensions of time may be obtained under 37 CFR 1.136(a). The date of have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the s set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corresponding amount hortened statutory period for reply orig	of the fee. The appropria inally set in the final Offic	ate extension fee e action; or (2) as
2. The Notice of Appeal was filed on . A brief in compl	liance with 37 CFR 41.37 must be	filed within two month	s of the date of
filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed wi AMENDMENTS	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
3. The proposed amendment(s) filed after a final rejection, b	out prior to the date of filing a brief,	will not be entered be	cause
(a) ☐ They raise new issues that would require further cor	•	TE below);	
(b) They raise the issue of new matter (see NOTE below	•		
(c) They are not deemed to place the application in bett	er form for appeal by materially re	ducing or simplifying t	ne issues for
appeal; and/or (d) ☐ They present additional claims without canceling a c	parraepanding number of finally rei	acted claims	
NOTE: (See 37 CFR 1.116 and 41.33(a)).	orresponding number of finally reju	scied ciaims.	
4. The amendments are not in compliance with 37 CFR 1.12	21 See attached Notice of Non Co	mpliant Amondment (DTOL 324)
5. Applicant's reply has overcome the following rejection(s):		mpliant Amendment (F 10L-324).
6. Newly proposed or amended claim(s) would be all		timaly filed amondmor	at cancaling the
non-allowable claim(s).	owable ii subifilited iii a separate,	uniely filed afficilatile	it canceling the
7. For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows:		ll be entered and an e	xplanation of
Claim(s) allowed:			
Claim(s) objected to: Claim(s) rejected:			
Claim(s) withdrawn from consideration:			
AFFIDAVIT OR OTHER EVIDENCE			
8. The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).			
9. The affidavit or other evidence filed after the date of filing a entered because the affidavit or other evidence failed to or showing a good and sufficient reasons why it is necessary	vercome <u>all</u> rejections under appea	al and/or appellant fail	s to provide a
10. The affidavit or other evidence is entered. An explanation	n of the status of the claims after e	ntry is below or attach	ed.
REQUEST FOR RECONSIDERATION/OTHER			
 11. The request for reconsideration has been considered but See Continuation Sheet. 12. Note the attached Information Displaying Statement(a) 		n condition for allowan	ce because:
12. Note the attached Information <i>Disclosure Statement</i>(s). (13. ☐ Other:	F10/36/06) Paper No(s)		
/David K Moore/	/Neil R. McLean/		
Supervisory Patent Examiner, Art Unit 2625	Examiner, Art Unit 2625		

Continuation of 11. does NOT place the application in condition for allowance because: Regarding Applicant's Argument: "Yoshiba does not compare pixels of the second frame buffer memory against the pixels of the first frame buffer memory to determine whether changes have been made to the pixels of the first frame buffer memory, in contradistinction to the claimed invention" and "Yoshiba does not disclose anywhere that its controller ever compares the display data (i.e., pixels) of the second memory against the display data (i.e., pixels) of the first memory to determine whether changes have been made to the display data of the first memory, in contradistinction to the claimed invention."

Examiner's Response:

The Examiner respectfully disagrees with the applicant for the following reasons:

Toshiba discloses a display memory control system. Figure 1 is a schematic block diagram of a display memory control system. Toshiba discloses a first and second memory (VRAM 16 and VRAM 24) which are controlled by controller CRTC 22 as described in more detail by Yoshiba at Column 3, lines 47-65.

Toshiba discloses a controller that compares, specifically Toshiba discloses in Figure 12, and at Column 7, lines 45-55 an address comparator circuit 300 which comprises two comparators 300a and 300b.

Toshiba further discloses the process by which content is updated if a particular region has changed (Column 8, lines 1-24 and Figure 8). Lastly, Toshiba discloses motivation for only updating the pixels which have changed: to save time and to improve the display by reducing flicker because of a reduction in the 'writing period' (Column 1; lines 19-43).